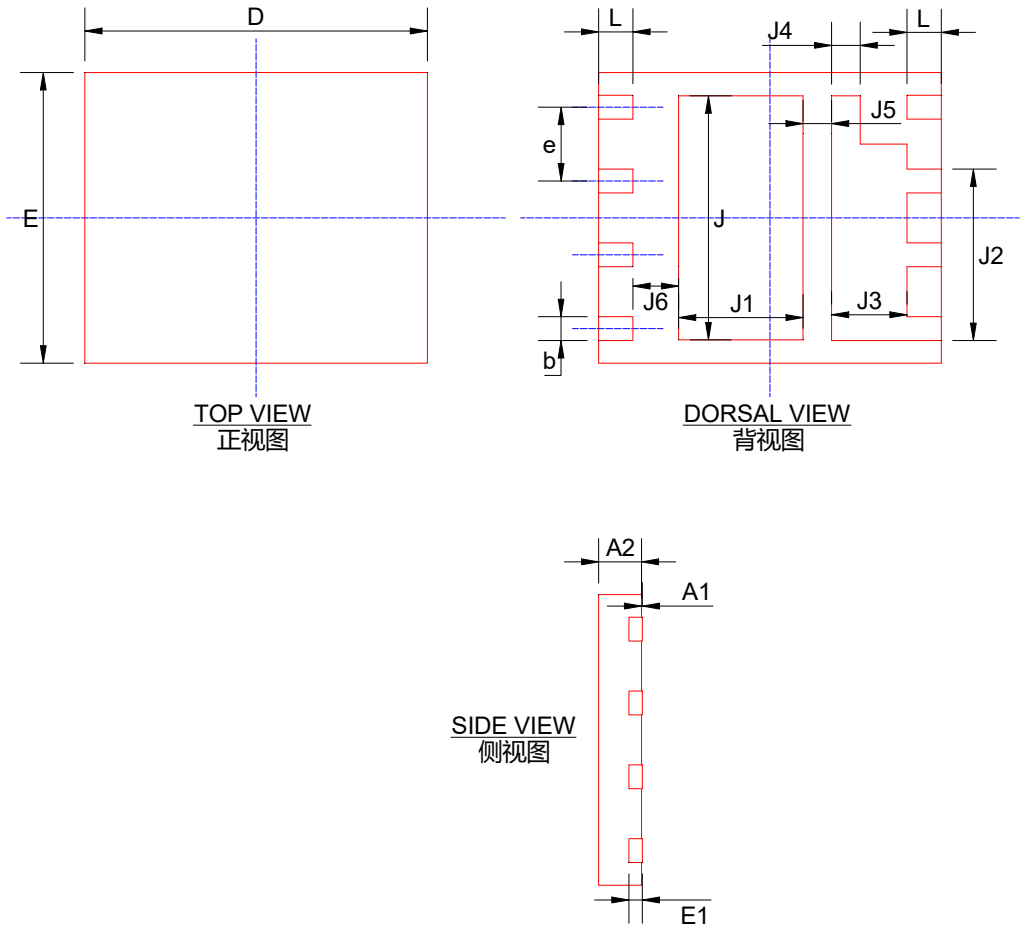


	深圳市锐骏半导体有限公司 ShenZhen Ruichips Semiconductor CO.,LTD	编号	RU-001-DFN5X6-8L-A -002
	封装外形尺寸（POD）规范	版本	A/1
		页次	4

DFN5X6-8L(双基岛)



- NOTE:
- 1: ALL UNITS ARE IN MILLIMETER.
 - 2: EJECTOR PIN MARK POSITION MAY VARY FROM DIFFERENT MOLD.
 - 3: ALL DIMENSIONS REFER TO JEDEC.DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.

SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
A1	-	-	0.015	-	-	0.001
A2	0.700	0.750	0.800	0.028	0.030	0.031
E1	0.185	0.235	0.285	0.007	0.009	0.011
b	0.360	0.410	0.460	0.014	0.016	0.018
e	1.220	1.270	1.320	0.048	0.050	0.052
D	5.900	6.000	6.100	0.232	0.236	0.240
E	4.900	5.000	5.100	0.193	0.197	0.201
J	4.150	4.200	4.250	0.163	0.165	0.167
J1	2.120	2.170	2.220	0.083	0.085	0.087
J2	2.900	2.950	3.000	0.114	0.116	0.118
J3	1.270	1.320	1.370	0.050	0.052	0.054
J4	0.450	0.500	0.550	0.018	0.020	0.022
J5	0.450	0.500	0.550	0.018	0.020	0.022
J6	0.760	0.810	0.860	0.030	0.032	0.034
L	0.550	0.600	0.650	0.022	0.024	0.026